

**IN THE SPECIFICATION:**

Please amend the specification as follows:

On page 1, line 1, please remove the underline from the section heading by replacing the section heading as follows:

A1      [[BACKGROUND OF THE INVENTION]] BACKGROUND OF THE INVENTION

On page 1, line 3, please remove the underline from the section heading by replacing the section heading as follows:

A2      [[RELATED APPLICATION]] RELATED APPLICATIONS

On page 1, line 11, please remove the underline from the section heading by replacing the section heading as follows:

A3      [[FIELD OF THE INVENTION]] FIELD OF THE INVENTION

On page 2, line 1, please remove the underline from the section heading by replacing the section heading as follows:

A4      [[BACKGROUND ART]] BACKGROUND ART

On page 2, line 11, please remove the underline from the section heading by replacing the section heading as follows:

A5      [[Tree Data Structure]] Tree Data Structure

On page 3, line 4, please remove the underline from the section heading by replacing the section heading as follows:

A6      [[Tree Modification]] Tree Modification

On page 3, line 13, please remove the underline from the section heading by replacing the section heading as follows:

A7      [[File Tree Reconciler]] File Tree Reconciler

On page 7, line 1, please remove the underline from the section heading by replacing the section heading as follows:

A8      [[SUMMARY OF THE INVENTION]] SUMMARY OF THE INVENTION

On page 8, line 1, please remove the underline from the section heading by replacing the section heading as follows:

A9      [[BRIEF DESCRIPTION OF THE DRAWINGS]] BRIEF OF DESCRIPTION OF THE  
DRAWINGS

On page 9, line 1, please remove the underline from the section heading by replacing the section heading as follows:

A10  
[[DETAILED DESCRIPTION OF THE INVENTION]] DETAILED DESCRIPTION OF  
THE INVENTION

On page 9, line 10, please remove the underline from the section heading by replacing the section heading as follows:

A11  
[[File Tree Reconciler]] File Tree Reconciler

On page 16, line 1, please remove the underline from the section heading by replacing the section heading as follows:

A12  
[[Embodiment of a Computer Execution Environment]] Embodiment of a Computer  
Execution Environment

Please amend the paragraph beginning on page 1, line 5 as follows:

A13  
This application claims the benefit of co-pending United States Provisional Patent Application No. 60/295,987 filed on June 4, 2001, and co-pending United States Non-Provisional Patent Application No. [[\_\_\_\_]] 10/021,943 filed on [[\_\_\_\_]] December 12, 2001 ~~claiming priority to the above mentioned Non Provisional Application,~~ the disclosures of which are hereby incorporated by references.

Please amend the paragraph beginning on page 3, line 15 as follows:

A14

In order to reconcile any differences between an old and a new file tree, the two trees have to be compared. A utility, commonly called a comparator, compares the two file tree descriptions and generates a sequenced log of changes that transforms the old tree to a new tree. A complete description of the type of file tree comparator is contained in co-pending provisional U.S. patent application "File Tree Comparator", Sr. No. 60/296,065, filed June 4, 2001, and co-pending non-provisional U.S. patent application "File Tree Comparator", Sr. No. [[\_\_\_\_]] 10/021,943, filed on [[\_\_\_\_]] December 12, 2001, and assigned to the assignee of this patent application. After the changes have been recorded, another utility, commonly known as a reconciler, takes in as its input the log of changes (if one is available) from both the old and the new file trees and reconciles any changes that have occurred since the last synchronization.

Please amend the paragraph beginning on page 18, line 14 as follows:

A15

In one embodiment of the invention, the processor 613 is a microprocessor manufactured by ~~Motorola~~ MOTOROLA, such as the 680X0 processor or a microprocessor manufactured by ~~Intel~~ INTEL, such as the 80X86, or ~~Pentium~~ PENTIUM processor, or a SPARC microprocessor from Sun Microsystems, Inc. However, any other suitable microprocessor or microcomputer may be utilized. Main memory 615 is comprised of dynamic random access memory (DRAM). Video memory 614 is a dual-ported video random access memory. One port of the video memory 614 is coupled to video amplifier 616. The video amplifier 616 is used to drive the cathode ray tube (CRT) raster monitor 617. Video amplifier 616 is well known in the art and may be implemented by any suitable apparatus. This circuitry converts pixel data stored in video

A15 memory 614 to a raster signal suitable for use by monitor 617. Monitor 617 is a type of monitor suitable for displaying graphic images.

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